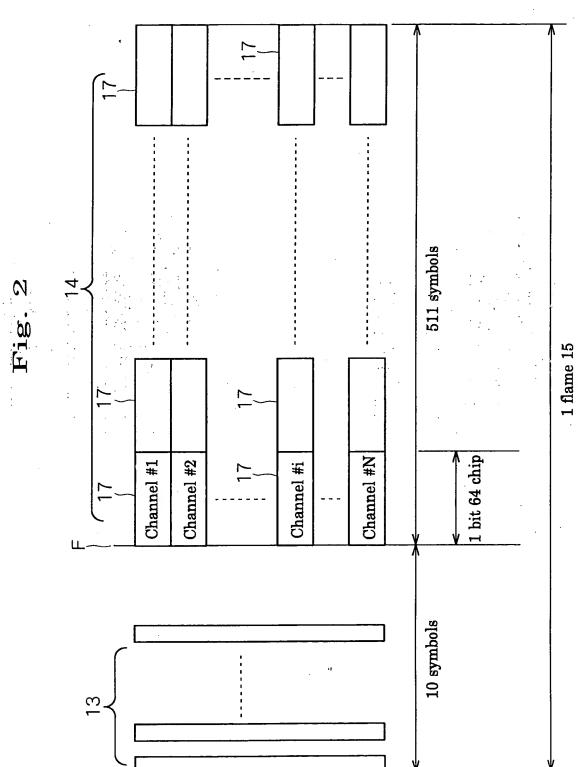
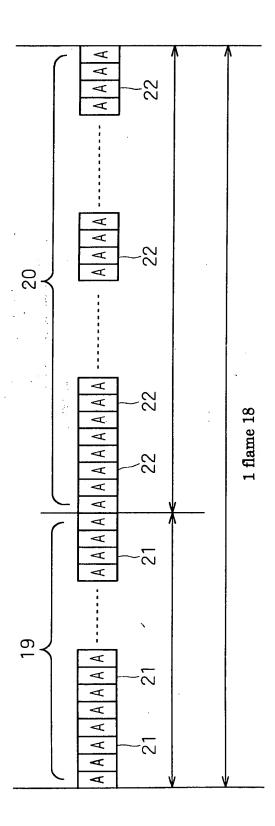
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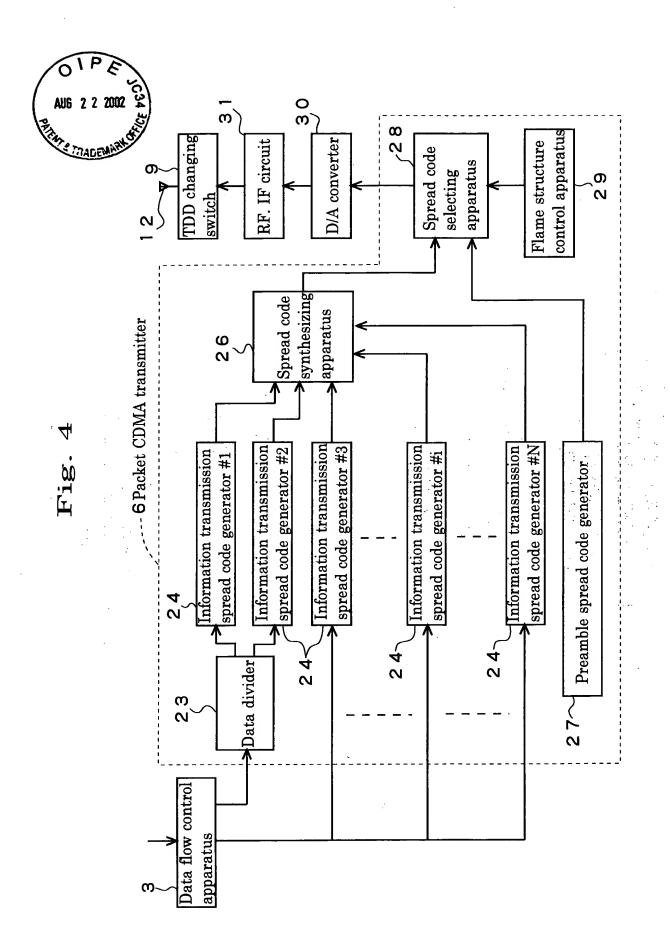
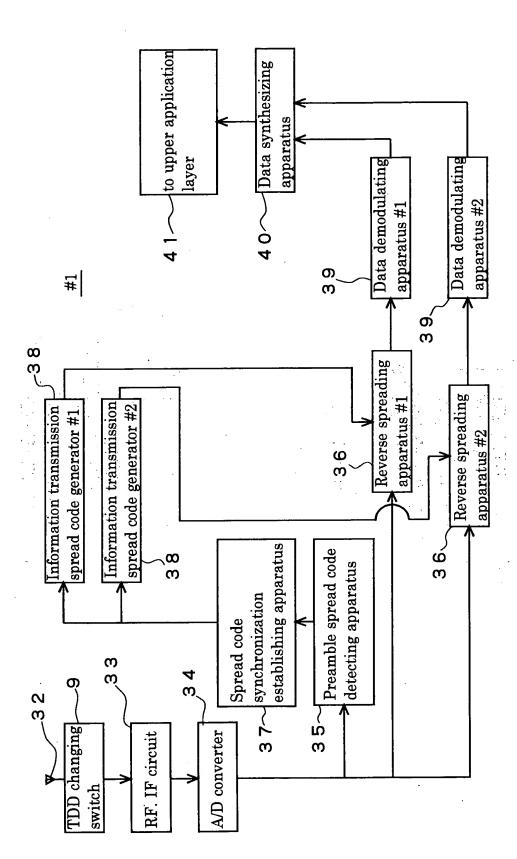
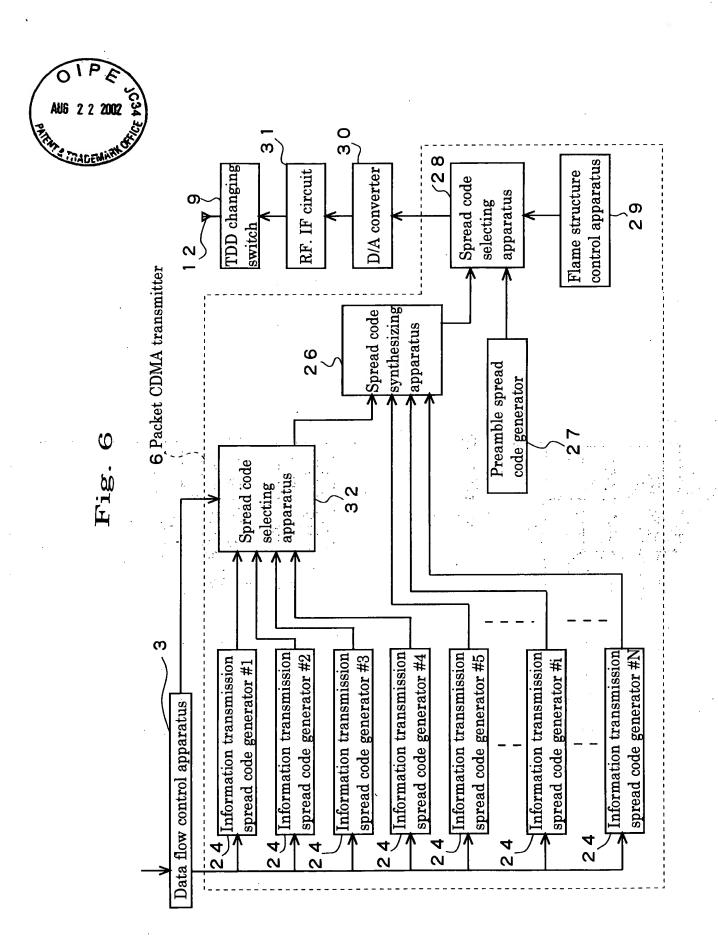


Fig. 5





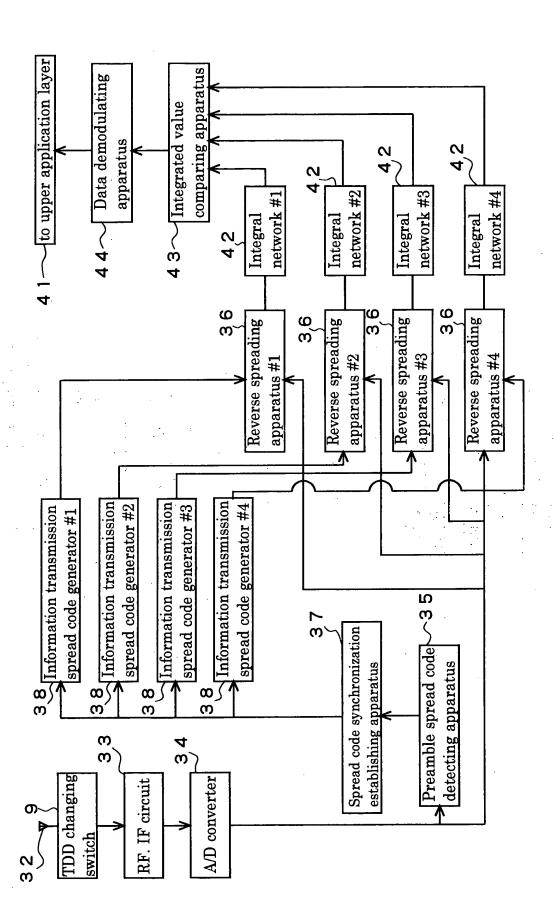


Fig. 7

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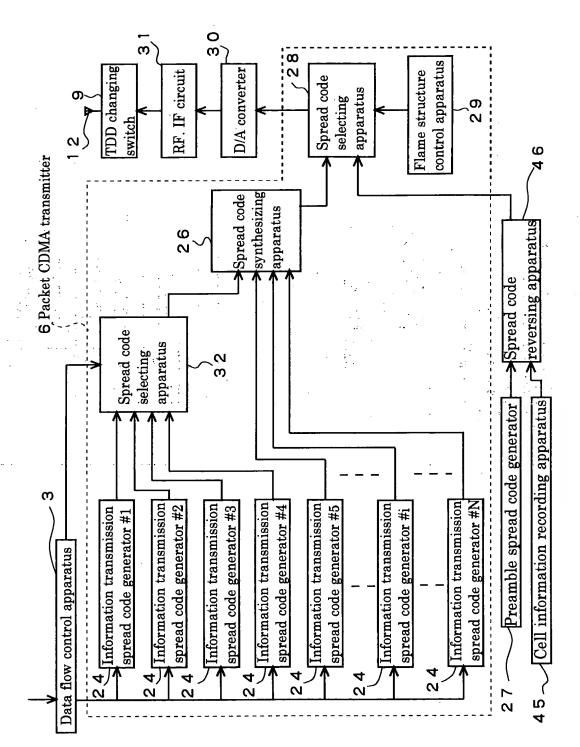
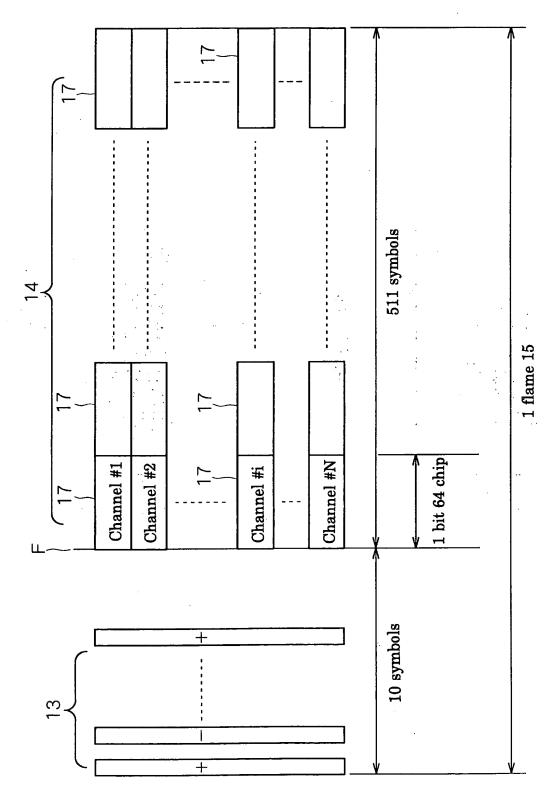
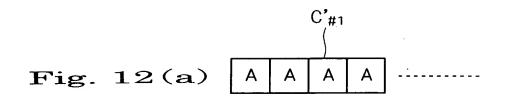
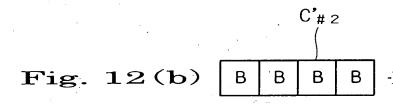


Fig. 9



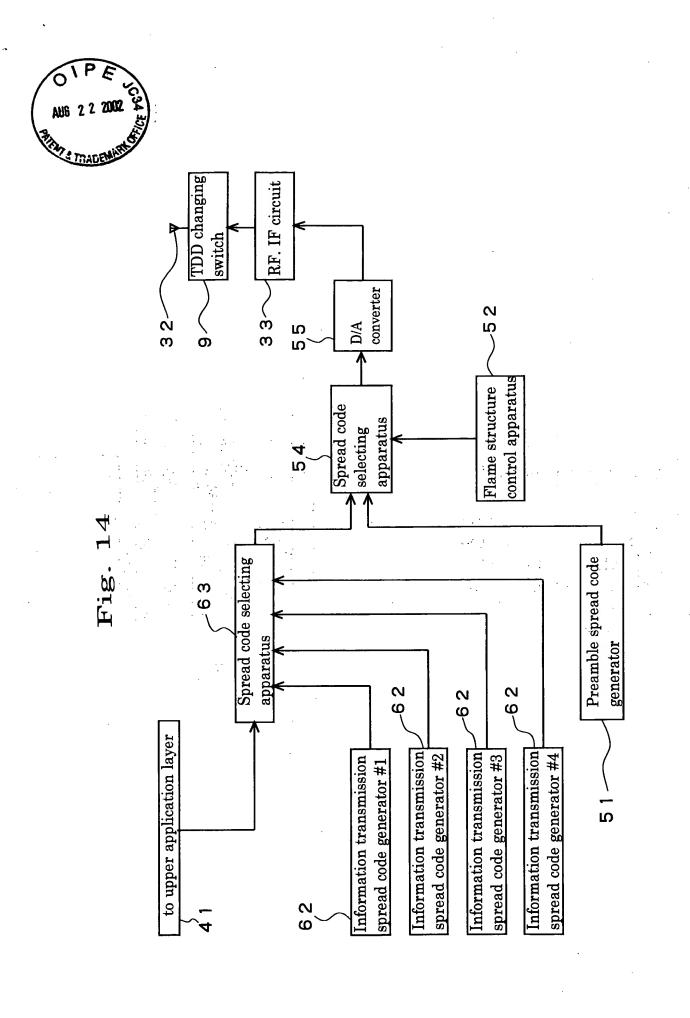


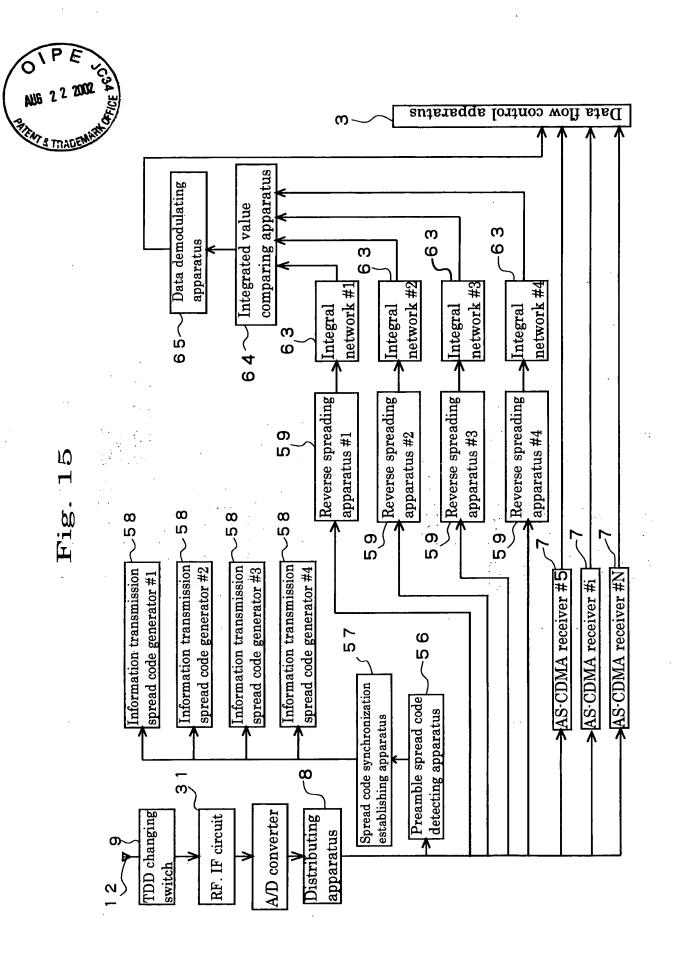




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Fig. 17(a)

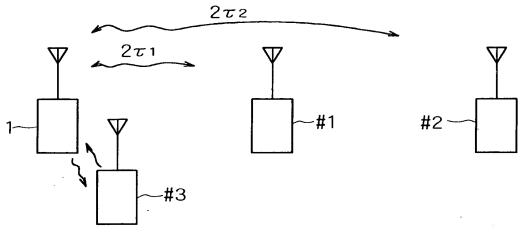


Fig. 17(b)

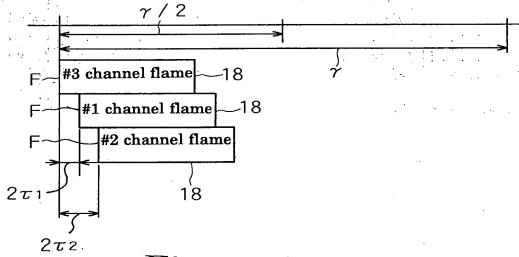
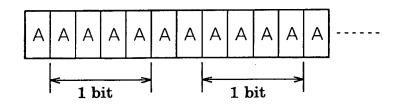


Fig. 17(c)



Downlink transmitter

Other AS-CDMA receiver

7#i (i=2,3,...,N)



Fig. 20(a)

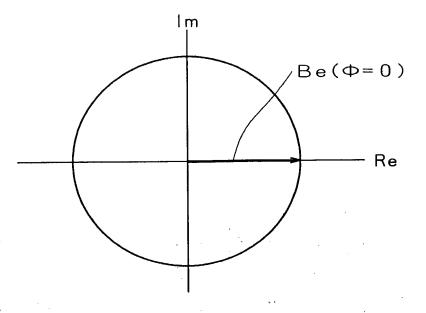
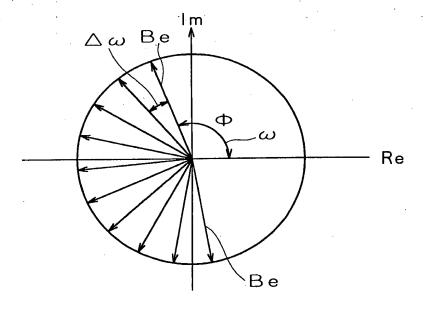


Fig. 20(b)



Data demodulating ➤ Phase correcting application layer თ ღ 8 apparatus to upper ✓ circuit Reverse spreading Absolute phase detecting circuit Information transmission apparatus ထ spread code generator ယ် ပာ <u>ထ</u> က establishing apparatus Preamble spread code 37 detecting apparatus Correlation peak judging circuit synchronization 7,6 Spread code <u>က</u> ယ ത A/D converter TDD changing RF. IF circuit switch 34

Fig. 21